

Electronic Patent Application Fee Transmittal

Application Number:	10620151				
Filing Date:	15-Jul-2003				
Title of Invention:	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line				
First Named Inventor/Applicant Name:	Matthew A. Kliesner				
Filer:	David L. Stewart/Lois Love				
Attorney Docket Number:	72206				
Filed as Large Entity					
Utility Filing Fees					
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Basic Filing:					
Pages:					
Claims:					
Miscellaneous-Filing:					
Petition:					
Patent-Appeals-and-Interference:					
Post-Allowance-and-Post-Issuance:					
Extension-of-Time:					

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Request for continued examination	1801	1	810	810
Total in USD (\$)				810